

**AMENDED CLAIMS**

The pending claims as amended are as follows, set forth herein as per the new amendment procedure.

1-15. (canceled)

16. (previously presented) A multilayer printed circuit board with at least one through hole perpendicular to the plane of the layers of the multilayer printed circuit board, said multilayer printed circuit board having at least four trace layers with foil conductors therein and non-conductive materials between each of said four trace layers, said multilayer printed circuit board having lodged within said through hole two conductive stakes, each of said two conductive stakes having an inner side, the inner side of each stake directed toward a center of said through hole, there being a space within said through hole separating said inner sides of said two stakes, said two conductive stakes providing electrical contact to at least two conductive foils in two of said trace layers separated by a layer of non-conductive material, thus establishing by each said conductive stake an electrical pathway between said foils.

17. (previously presented) A multilayer printed circuit board with at least two partial through holes, aligned in a perpendicular axis to the plane of the layers of the multilayer printed circuit board, said multilayer printed circuit board having at least four trace layers with foil conductors therein and non-conductive materials between each of said four trace layers, said multilayer printed circuit board having lodged within each said partial through hole a conductive stake, each of said two conductive stakes having an inner side, the inner side of each stake directed toward the partial through hole in which the other conductive stake is lodged, there being at least a portion of a layer of said multilayer circuit board separating said inner sides of said two conductive stakes, said two conductive stakes providing electrical contact to at least two conductive foils in two of said trace layers separated by a layer of non-conductive material, thus establishing by each said conductive stake an electrical pathway between said foils on opposing sides of said multilayer circuit board, separated by said at least a portion of a layer.

18. (previously presented) The circuit board of claim 16 wherein the conductive stake has a longitudinal polygonal shape such that at least one corner of said polygonal

shape when inserted into said through hole extend beyond an internal diameter so as to establish electrical contact with the foils.

19. (previously presented) The circuit board of claim 17 wherein at least one of the conductive stakes has a longitudinal polygonal shape such that at least one corner of said polygonal shape when inserted into said partial through hole extend beyond an internal diameter of said partial through hole so as to establish electrical contact with the foils.

20. (currently amended) A printed circuit board comprising:

at least three layers of material, such that two of the layers of material are electrically conductive and the third layer is an electrical insulator and wherein the insulating layer is disposed between the conductive layers; and

at least one conductive stake inserted into a substantially round through hole of a certain diameter established in the printed circuit board for forming a via or electrically connecting foils from the two conductive layers together;

wherein the conductive stake has a longitudinal filled polygonal shape when viewed in cross-section such that at least one corner of said polygonal shape when inserted into said through hole extend beyond said certain diameter so as to establish contact with the foils for forming the via.

21. (previously presented) The circuit board of claim 20 wherein said polygonal shape is a pentagon.